

Electronic Acknowledgement Receipt

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Application Number:	10731714
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Title of Invention:	At-speed ATPG testing and apparatus for SoC designs having multiple clock domain using a VLCT test platform
First Named Inventor:	Anupama Aniruddha Agashe
Customer Number:	23494
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Request for Corrected Filing Receipt	T36268ReqCorrectedFilingReceipt.pdf	2540	no	1

Warnings:					
Information:					
2	Request for Corrected Filing Receipt	T36268FilingReceipt.pdf	103129	no	2
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